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Marshall Space Flight Center



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Computer-Aided Design of Large-Scale Integrated Circuits: A Concept

A computer study effort has been undertaken to develop a software program for the computer-aided design of the photographic masks necessary for integrated circuit fabrication. The circuit design and mask development sequence have been considerably improved by the use of a general purpose computer with an interactive graphics capability that establishes an efficient two-way communications link between the design engineer and the system. The interactive graphics capability places the design engineer in direct control of circuit development.

The final report of the study contains a discussion of: the functional design of the baseline software; the processor requirements, including initial storage estimates and input/output requirements; the designer/computer communication interface, both with and without a graphics terminal; and the standardized placement and interconnect ground rules established during the study phase.

Although the functional design of the computer program was accomplished under the constraint of using an available small system, additional capability can readily be developed with the established design concepts.

Note:

Requests for further information may be directed to:

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No patent action is contemplated by NASA.

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